

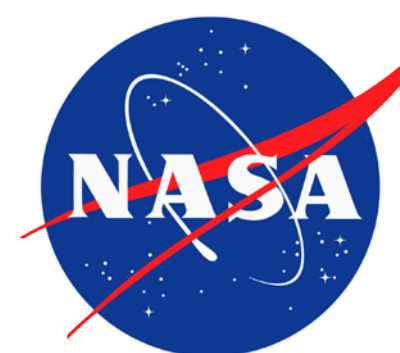


Martha O'Bryan

Compendium of Single Event Effect Results from NASA Goddard Space Flight Center

Martha V. O'Bryan¹, Kenneth A. LaBel², Carl M. Szabo¹, Dakai Chen², Michael J. Campola², Megan C. Casey², Jean Marie Lauenstein², Edward P. Wilcox¹, Raymond L. Ladbury², Stanley A. Ikpe³, Jonathan A. Pellish,² and Melanie D. Berg¹

National Aeronautics and
Space Administration



1. AS&D, Inc.; 2. NASA GSFC; 3. NASA LaRC

Abstract: We present the results of single event effects (SEE) testing and analysis investigating the effects of radiation on electronics. This paper is a summary of test results.

Introduction

NASA spacecraft are subjected to a harsh space environment that includes exposure to various types of ionizing radiation. The performance of electronic devices in a space radiation environment are often limited by their susceptibility to single event effects (SEE). Ground-based testing is used to evaluate candidate spacecraft electronics to determine risk to spacecraft applications. Interpreting the results of radiation testing of complex devices is challenging. Given the rapidly changing nature of technology, radiation test data are most often application-specific and adequate understanding of the test conditions is critical.

Studies discussed herein were undertaken to establish the application-specific sensitivities of candidate spacecraft and emerging electronic devices to single-event upset (SEU), single-event latchup (SEL), single-event gate rupture (SEGR), single-event burnout (SEB), and single-event transient (SET).

For total ionizing dose (TID) and displacement damage dose (DDD) results, see a companion paper submitted to the 2016 Institute of Electrical and Electronics Engineers (IEEE) Nuclear and Space Radiation Effects Conference (NSREC) Radiation Effects Data Workshop (REDW) entitled "Compendium of Total Ionizing Dose and Displacement Damage Results from NASA Goddard Space Flight Center" by M. Campola, *et al.*

Test Techniques and Setup

A. Test Facilities

All tests were performed between February 2015 and February 2016. Heavy ion experiments were conducted at the Lawrence Berkeley National Laboratory (LBNL), NASA Space Radiation Laboratory (NSRL) at Brookhaven National Laboratory, and at the Texas A&M University Cyclotron (TAMU). All of these facilities provide a variety of ions over a range of energies for testing. Each device under test (DUT) was irradiated with heavy ions having linear energy transfer (LET) ranging from 0.07 to 120 MeV^{cm}/mg. Fluxes ranged from 1x10⁷ to 1x10⁸ particles/cm²/s, depending on device sensitivity. Representative ions used are listed in Tables I, II and III. LETs in addition to the values listed were obtained by changing the angle of incidence of the ion beam with respect to the DUT, thus changing the path length of the ion through the DUT and the "effective LET" of the ion. Energies and LETs available varied slightly from one test facility to another.

Proton SEE tests were performed at Northwestern Medicine Chicago Proton Center (COH), Hampton University Proton Therapy Institute (HUPTI), Mass General Francis H. Burr Proton Therapy (MGH), Scripps Proton Therapy Center (Scripps), and Tri-University Meson Facility (TRIUMF).

Laser SEE tests were performed at the pulsed laser facility at the Naval Research Laboratory (NRL). Single photon absorption method was used with the laser light having a wavelength of 530 nm resulting in a skin depth (depth to which the light intensity decreased to 1/e – or about 37% – of its intensity at the surface) of ~2 μm. A nominal pulse rate of 1 kHz was utilized. Pulse width was 1 ps, beam spot size ~1.2 μm.

Table I: LBNL Test Heavy Ions

Ion	Energy (MeV)	Surface LET in Si (MeV ^{cm} /mg) (Normal Incidence)	Range in Si (μm)
¹⁶ O	183	2.2	226
²² Ne	216	3.5	275
⁴⁰ Ar	400	9.7	130
²³ V	508	14.6	113
⁶⁵ Cu	660	21.2	108
⁸⁴ Kr	906	30.2	113
¹⁰⁷ Ag	1039	48.2	90
¹²⁹ Xe	1233	58.8	90

LBNL 10 MeV per amu tune

Table III: NSRL Test Heavy Ions

Ion	Energy (MeV)	Surface LET in Si (MeV ^{cm} /mg) (Normal Incidence)	Range in Si (μm)
¹⁹⁷ Au	32505	24.7	3700

Table II: TAMU Test Heavy Ions

Ion	Energy (MeV)	Surface LET in Si (MeV ^{cm} /mg) (Normal Incidence)	Range in Si (μm)
¹⁴ N	210	1.3	428
²⁰ Ne	300	2.5	316
⁴⁰ Ar	599	7.7	229
⁶⁵ Cu	944	17.8	172
⁸⁴ Kr	1259	25.4	170
¹⁰⁹ Ag	1634	38.5	156
¹²⁹ Xe	1934	47.3	156
¹³⁷ La	2954	80.2	155

TAMU 15 MeV per amu tune

⁸⁹ Kr	2081	19.8	332
⁸¹ Br	3197	38.9	286
¹³⁷ Xe			

amu = atomic mass unit

Test Results Overview

Principal investigators are listed in Table IV. Abbreviations and conventions are listed in Table V. SEE results are summarized in Table VI. Unless otherwise noted all LETs are in MeV^{cm}/mg and all cross sections are in cm²/device. All SEL tests are performed to a fluence of 1x10⁷ particles/cm² unless otherwise noted.

Table IV: List of Principal Investigators

Principal Investigator (PI)	Abbreviation
Melanie D. Berg	MB
Megan C. Casey	MCC
Michael J. Campola	MJC
Dakai Chen	DC
Raymond L. Ladbury	RL
Jean-Marie Lauenstein	JML
Carl M. Szabo	CS
Jonathan A. Pellish	JP
Edward (Ted) P. Wilcox	TV

Table V: Abbreviations and Conventions

LET = linear energy transfer (MeV^{cm}/mg)
LET_{tr} = linear energy transfer threshold (the maximum LET value at which no effect was observed at an effective fluence of 1x10⁷ particles/cm² – in MeV^{cm}/mg)
> = SEE observed at lowest tested LET
> = no SEE observed at highest tested LET
α = cross section (cm²/device, unless specified as cm²/bit)
α_{max} = cross section at maximum measured LET (cm²/device, unless specified as cm²/bit)
ADC = analog to digital converter
BICMOS = bipolar complementary metal oxide semiconductor
BNL=Brookhaven National Laboratory/Tandem Van de Graaff
COH=Northwestern Medicine Chicago Proton Center
CMOS = complementary metal oxide semiconductor
CMRR = common-mode rejection ratio
DAC=Digital to Analog Converter
DUT = device under test
ECC = error correcting code
H = heavy ion test
HUPTI=Hampton University Proton Therapy Institute
ID# = identification number
I_{DS} = drain-source current
I_g = gate current
I_{out} = output current
I_{rev} = reverse leakage current
L = laser test
LBNL = Lawrence Berkeley National Laboratory
LDC = LUT data code
LVDS=Low-Voltage Differential Signaling
min = minimum
MGH=Mass General Francis H. Burr Proton Therapy
MOSFET = metal-oxide-semiconductor field-effect transistor
NAND = Negated AND or NOT AND
NRL = Naval Research Laboratory
PI = principal investigator
PGS = post-irradiation gate stress
PSRR = power supply rejection ratio
REAG = radiation effects and analysis group
SEB = single-bit upset
Scripps = Scripps Proton Therapy Center
SEB = single event burnout
SEDR = single event dielectric rupture
SEE = single event effect
SEFI = single event functional interrupt
SEGR = single event gate rupture
SET = single event latchup
SEU = single event upset
SiC = silicon carbide
SMU = source-measure unit
TAMU = Texas A&M University Cyclotron Facility
TRIUMF=Tri-University Meson Facility
V_{CC} = power supply voltage
VDMOS = vertical double diffused MOSFET
V_{DS} = drain-source voltage
V_{GS} = gate-to-source voltage
V_{NAND} = vertical-NAND
V_{rev} = reverse bias voltage

B. Test Method

Unless otherwise noted, all tests were performed at room temperature and with nominal power supply voltages. Device qualification include SEL high-temperature, V_{CC} plus worst-case and for SEU/SET high-temperature, V_{CC} minus worst-case. Unless otherwise noted, SEE testing was performed in accordance with JESD57 test procedures where applicable.

1) SEE Testing - Heavy Ion:

Depending on the DUT and the test objectives, one or more of three SEE test approaches were typically used:

Dynamic – the DUT was continually exercised while being exposed to the beam. The events and/or bit errors were counted, generally by capturing with a high-speed oscilloscope, digital input/output (DIO) device, microprocessor, FPGA, or by comparing the DUT output to an unirradiated reference device or with an expected output (Golden chip or virtual Golden chip methods). In some cases, the effects of clock speed or device operating modes were investigated. Results of such tests should be applied with caution due to their application-specific nature.

Static – the DUT was configured prior to irradiation; data were retrieved and errors were counted after irradiation.

Biased – the DUT was biased and clocked while power consumption was monitored for SEL or other destructive effects. In most SEL tests, functionality was also monitored.

DUTs were monitored for soft errors, such as SEUs, and for hard failures such as SEGR. Detailed descriptions of the types of errors observed are noted in the individual test reports on radhome.nasa.gov and nepp.nasa.gov.

SET testing was performed using high-speed oscilloscopes controlled via LabVIEW®. Individual criteria for SETs are specific to the device and application being tested.

Heavy ion SEE sensitivity experiments include measurement of the linear energy transfer threshold (LET_{tr}) and cross section at the maximum measured LET. The LET_{tr} is defined as the maximum LET value at which no effect was observed at an effective fluence of 1x10⁷ particles/cm². In the case where events are observed at the smallest LET tested, LET_{tr} will either be reported as less than the lowest measured LET or determined approximately as the LET parameter from a Weibull fit. In the case of SEGR and SEB experiments, measurements are made of the SEGR or SEB threshold V_{DS} (drain-source voltage) as a function of LET and ion energy at a fixed V_{GS} (gate-to-source voltage).

2) SEE Testing - Proton:

Proton SEE tests were performed in a manner similar to heavy ion experiments. However, because protons usually cause SEE via indirect ionization of recoil particles, results are parameterized in terms of proton energy rather than LET. Because such proton-induced nuclear interactions are rare, proton tests also feature higher cumulative fluences and particle flux rates than heavy ion experiments.

2) SEE Testing - Pulsed Laser Facility Testing:

The DUT was mounted on an X-Y-Z stage in front of a 100x lens that produces a spot diameter of approximately 1 μm at full-width half-maximum (FWHM). The X-Y-Z stage can be moved in steps of 0.1 μm for accurate determination of SEE-sensitive regions in front of the focused beam. An illuminator, together with a charge-coupled device (CCD) camera and monitor, were used to image the area of interest thereby facilitating accurate positioning of the device in the beam. The pulse energy was varied in a continuous manner using a polarizer-half-waveplate combination and the energy was monitored by splitting off a portion of the beam and directing it at a calibrated energy meter.

Table VI: Summary of SEE Test Results

	Part Number	Manufacturer	LDC or Wafer ID#	Device Function	Technology	Particle (Facility/Year/Month) P.1	Test Results: LET in MeV/cm ² or in cm ² /device, unless otherwise specified	Supply Voltage	Supply Current	Supply Power
Processors:										
	Broadwell® 9 th Gen. Core™ i3-5005U	Intel	15-080	Processor	14nm Gen 5 CMOS and FinFET	P. (MGT, TRIUMF, HUPTI, Scripps) CS	Testing to evaluate Proton facilities and development of test procedures. Test results available via Duncom, et al., at this year's Data Workshop.	1.05V, 3.3V	10	
	SkyLake 6 th Gen. Core™ i5-6500K	Intel	15-081	Processor	14nm Gen 6 CMOS and FinFET	P. (TRIUMF15Nov)	Testing to evaluate Proton facilities and development of test procedures. Test results available via Duncom, et al., at this year's Data Workshop.	3.3V, 5V, 12V	1	
	SkyLake 6 th Gen. Core™ i3-6100	Intel	15-081	Processor	14nm Gen 6 CMOS and FinFET	H. (TAMU12Dec)	Test results available via Duncom, et al., at this year's Data Workshop.	3.3V, 5V, 12V	1	
	SkyLake 6 th Gen. Core™ i3-6100T	Intel	15-081	Processor	14nm Gen 6 CMOS and FinFET	H. (TAMU18May) CS	Testing to evaluate Proton facilities and development of test procedures. Test results available via Duncom, et al., at this year's Data Workshop.	3.3V, 5V, 12V	2	
Memory Devices:										
	MT29F1280GCECB96	Micron	201448, 14-088	Flash Memory	16 nm CMOS	H. (LBNL2015Aug, 15Dec)	SEU LET _{tr} < 0.9 MeV/cm ² SEU LET _{tr} = 1.7 MeV/cm ² at LET of SE. SEFI: Part is vulnerable to SEFI in static based and dynamic test modes. SEFI LET _{tr} < 0.9 MeV/cm ² No device functional failure up to LET of 118. However observed device failure at LET of 58.	3.3V	2	
SI Power Devices:										
	BU171C523-01 Engineering samples	Infineon	1440.60, 14-076	MOSFET	Super-junction	H. (TAMU2015Nov21) JML	Primary failure mode: SEGR 2074-MeV Ta LET<77. Pass 150 V _{DS} at 0 to +10 V _{GS} max 2074-MeV Ta LET<77. Pass 150 V _{DS} at 0 to +10 V _{GS} max SEB LET _{tr} > 94	V _{DS} = 6V to +20V V _{GS} = 20V to +20V +115V	5	
	DG403	Vishay	G13744A, 15-018	Analog Switch	CMOS	H. (LBNL2015Aug11) MCC/Boate	Primary failure mode: SEGR 1634-MeV Au LET<440. Pass 150 V _{DS} at 0 to +10 V _{GS} max 1634-MeV Au LET<440. Pass 150 V _{DS} at 0 to +10 V _{GS} max	0 V to +2	2	
	2N6790	International Rectifier	1427, 15-002	MOSFET	Power	H. (LBNL2015Aug11) MCC/Boate	Primary failure mode: SEGR 1634-MeV Au LET<440. Pass 150 V _{DS} at 0 to +10 V _{GS} max 1634-MeV Au LET<440. Pass 150 V _{DS} at 0 to +10 V _{GS} max	0 V to +2	2	
	2N6845	International Rectifier	1427, 15-002	MOSFET	Power	H. (TAMU2015Apr11) MCC/DynMCC	Primary failure mode: SEGR 1634-MeV Au LET<440. Pass 150 V _{DS} at 0 to +10 V _{GS} max 1634-MeV Au LET<440. Pass 150 V _{DS} at 0 to +10 V _{GS} max	0 V to +2	2	
	LM195	National Semiconductor	15-031	Power Transistor	Bipolar	H. (TAMU2015Apr11) MCC	SEB LET _{tr} > 87 (2006-MeV Au)	35V	4	
	1N5554	Microsemi	13-026, 13-012, 14-009	Diode	Si	H. (NSRL12Mar) MCC	No degradation observed at 500V reverse voltage when irradiated with 30 V _{GS} at 100% of reverse voltage.	500V	62	
	DS517-06CR	IXYS	No LDC, 15-084	Diode	Si	H. (LBNL2015Dec18) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Degradation observed during beam run while based at 75% of reverse voltage. Post-irradiation electrical parameter measurements were out of specification. Catastrophic failure was observed at 100% of reverse voltage.	600 V	5	
	PS73100	Microsemi	0715, 14-004	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	100 V	3	
	FYF7145	Fairchild Semiconductor	13-044, 15-050	Diode	Si	H. (LBNL2015Jun27) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	45 V	3	
	FYF7205	Fairchild Semiconductor	13-044, 15-050	Diode	Si	H. (LBNL2015Jun27) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	45 V	3	
	FYF7206	Fairchild Semiconductor	13-044, 15-050	Diode	Si	H. (LBNL2015Jun27) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	45 V	3	
	FYF71010	Fairchild Semiconductor	13-044, 15-050	Diode	Si	H. (LBNL2015Jun27) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	100 V	3	
	MBR2445	Diodes, Inc.	1386, 15-054	Diode	Si	H. (LBNL2015Jun27) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	45 V	3	
	MBR2060	Diodes, Inc.	1386, 15-057	Diode	Si	H. (LBNL2015Jun27) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	60 V	3	
	MBR2020	Diodes, Inc.	1386, 15-090	Diode	Si	H. (LBNL2015Jun27) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	200 V	4	
	MBR4050	On Semiconductor	No LDC, 15-080	Diode	Si	H. (LBNL2015Dec18) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	250 V	5	
	MBRF2100	Diodes, Inc.	1386, 15-058	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	100 V	4	
	LMR3300	Power Integrations	1386, 15-059	Diode	Si	H. (LBNL2015Jun27) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	100 V	4	
	LA207000	Dioce, Inc.	1386, 15-073	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	600 V	11	
	LA207000	Power Integrations	No LDC, 15-075	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	600 V	11	
	VS-APR006-N3	Vishay	No LDC, 15-044	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	600 V	5	
	SEL8L40	Vishay	15-044, 15-044	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	40 V	3	
	SEL1045	Vishay	1472, 15-044	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	40 V	3	
	SEL1045	Diodes, Inc.	1386, 15-049	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	45 V	3	
	SEL1045	Vishay	1472, 15-044	Diode	Si	H. (LBNL2015Aug18) MCC	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	40 V	3	
	SBR20A300	Diodes, Inc.	No LDC, 15-050	Diode	Si	H. (LBNL2015Dec18) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	300 V	5	
	SBR30300	Diodes, Inc.	No LDC, 15-087	Diode	Si	H. (LBNL2015Dec18) MCC	No failures observed at 50% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	300 V	5	
SI Devices:										
	CPM2-1200-0025B	CREE	1327, 13-069, P1H13-15-067	MOSFET	Sic Gen 2 VDMOS	H. (LBNL2015Dec18) JML	996 MeV Xe (LET=46 in SiC). Immediate catastrophic SEB at 100 V _{DS} and 0 V _{GS} with fluence increasing with temperature. 996 MeV Xe (LET=46 in SiC). Immediate catastrophic SEB at 100 V _{DS} and 0 V _{GS} with fluence increasing with temperature. 996 MeV Xe (LET=46 in SiC). Immediate catastrophic SEB at 100 V _{DS} and 0 V _{GS} with fluence increasing with temperature. 996 MeV Xe (LET=46 in SiC). Immediate catastrophic SEB at 100 V _{DS} and 0 V _{GS} with fluence increasing with temperature.	0 V to +3	6	
	CPM3-3300 Engineering samples	CREE	84131C132, 15-060	MOSFET	Sic Gen 3 VDMOS	H. (TAMU2015Jun, LBNL2015Aug23) JML	No failures observed at 100% of reverse voltage when irradiated with 1233 MeV Xe (LET = 58.8 MeV/cm ²). Catastrophic failure was observed at 100% of reverse voltage.	0 V to +3	6	
	Test chip	GE	W004, 2410, 14-081	Diode	Sic IC	H. (TAMU2015Apr12, LBNL2015Dec18) JML	Contact PI for test results (data proprietary)	-100V	2	
	Engineering samples, various	GE	W004, 2410, 14-081	Diode	Sic diode	H. (TAMU2015Apr12) JML	Contact PI for test results (data proprietary)	Various	11	
	Engineering samples	GE	W004, 2410, 14-081	Diode	Sic VDMOS	HTYAMU2015Jun, TAMU2015Nov21) JML	Contact PI for test results (data proprietary)	12V-30V	12	
	STP5C10600	STMicroelectronics	15-041	Diode	Sic	H. (LBNL2015Aug23) JML	Contact PI for test results (data proprietary)	Frequency Divider	1	
	Test chip	GE	W004, 2410, 14-079	Frequency Divider	Sic IC	H. (TAMU2015Apr12) MCC/JML	Contact PI for test results (data proprietary)	12V-20V	5	
	Test chip	GE	W004, 2410, 14-081	Ring Oscillator	Sic IC	H. (TAMU2015Apr12, LBNL2015Dec18) MCC/JML	Contact PI for test results (data proprietary)	5V-20V	1	
	Test chip	GE	W004, 2410, 14-081	Op Amp	Sic IC	H. (TAMU2015Apr12, LBNL2015Dec18) MCC/JML	Contact PI for test results (data proprietary)	20V	2	
	IC test chip	Doak IC	14-066	Logic Device	Sic IC	H. (LBNL2015Jun27) MCC	Contact PI for test results (data proprietary)	10V	3	
Op Amps:										
	OPA2107	Texas Instruments	1144, 15-005	Op Amp	CMOS	H. (TAMU2015Apr11) MUC	The parts passed with supply voltages starting at +5V up to ±12V on an LET of 53 MeV/cm ² at an LET of 91 MeV/cm ² the passed from 2.0V to +10V.	Various	5	
	ADA038	Analog Devices	2467E, 15-039	Op Amp	XFCB	H. (TAMU2015Apr11) MUC	SEB/SEDR LET _{tr} < 63	+10V	3	
	LT2078	Linear Technology	1180, 15-024	Op Amp	Bipolar	H. (TAMU2015Apr11) MUC	SEB/SEDR LET _{tr} < 63	+10V	3	
	OP470	Analog Devices	1419A, 15-022	Op Amp	Bipolar	H. (LBNL2015Jun27) MUC/MC	SEDR LET _{tr} < 63 MeV/cm ² Normal mode input is not used. SEB or SEDR observed at V _{DD} = ±12 V above these conditions.	+6V to ±15 V	5	
	OP200	Analog Devices	9694, 15-039, 0738A	Op Amp	Bipolar	P. (HUPTI2015AS, CPO2015Sep) RLL	SEB/SEDR LET _{tr} < 100 MeV/cm ² at 120 MeV/cm ² or 80 MeV/cm ²			